#### IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Priority Application Serial No
Priority Filing Date
Inventor
Assignee Micron Technology, Inc.
Assignee
Priority Group Art Unit
Priority Examiner L. Malsawma
Attorney's Docket No MI22-2345
Title: Conductive Connection Forming Methods, Oxidation Reducing Methods, and Integrated
Circuits Formed Thereby

# INFORMATION DISCLOSURE STATEMENT PURSUANT TO 37 C.F.R. §§ 1.56, 1.97 AND 1.98

In compliance with 37 C.F.R. §§ 1.56, 1.97 and 1.98, your attention is directed to the United States patents and other references listed on the attached Form PTO-1449. No admission is made regarding whether all the submitted references are prior art.

The listed references were cited by, or submitted to, the Office in the parent, co-pending application of the above-identified application. The above-identified application is a continuation application of co-pending application Serial No. 09/518,511, filed March 3, 2000. Such prior disclosure is sufficient for the above-identified application as far as copies of the references are concerned. 37 C.F.R. § 1.98(d) and MPEP § 609(2).

Citation of these references is respectfully requested.

Respectfully submitted,

Dated: 15 July 2003

James E. Lake V Reg. No. 44,854

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	AA	4,393	3,096	7/83	Gajda				1		
	AB	5,071	1,714	12/91	Rodbell et al						
	AC	5,56	5,378	10/96	Harada et al						
	AD	5,656	6,860	8/97	Les						
	AE	6,030	0,895	2/00	Joshi et al						
	AF	6,110	0,819	8/00	Colgan et al						
	AG	6,110	0,829	8/00	Besser et al						
·	АН	6,140	0,236	10/00	Restaino et al						
	AI	6,146	6,988	11/00	Ngo et al						
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Priority SERIAL NO. 09/518,511

LIST OF ART CITED BY APPLICANT (Use several sheets if necessary)

APPLICANT Dinesh Chopra

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Priority SERIAL NO. 09/518,511

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	AD	5,547,881		08/96	Wang et al					
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